



Gokaraju Rangaraju Institute of Engineering and Technology  
(Autonomous)

**GR18  
D I-I S**

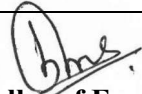
**M.Tech. I Year I Semester Supplementary Examinations (GR18) Time Table February/March 2025**

***Timings: 10.00 AM TO 01.00 PM***

| Date<br>Branch | 28/02/2025, FN<br>(Friday)   | 03/03/2025, FN<br>(Monday)   | 05/03/2025, FN<br>(Wednesday)                          | 07/03/2025, FN<br>(Friday)  | 10/03/2025, FN<br>(Monday)  | 12/03/2025, FN<br>(Wednesday)  |
|----------------|--|--|--|---|---|--|
| STRU<br>(20)   | <b>GR18D5164</b><br>Matrix Methods in<br>Structural Analysis                           | <b>GR18D5165</b><br>Advanced Solid<br>Mechanics                        | <b>GR18D5166</b><br>Advanced<br>Concrete<br>Technology | <b>GR18D5169</b><br>Analytical and<br>Numerical Methods for<br>Structural Engineering             | <b>GR18D5012</b><br>Research Methodology and<br>IPR<br>(Common to All Programs) | <b>GR18D5207</b><br>English for Research<br>Paper Writing<br>(Common to All<br>Programs) |
| PE<br>(43)     | <b>GR18D5049</b><br>Power Quality  | <b>GR18D5069</b><br>Electric Drive<br>Systems                          | <b>GR18D5070</b><br>Power Electronic<br>Converters     | <b>GR18D5053</b><br>Electric and Hybrid<br>Vehicles   | <b>GR18D5012</b><br>Research Methodology and<br>IPR<br>(Common to All Programs) | <b>GR18D5207</b><br>English for Research<br>Paper Writing<br>(Common to All<br>Programs) |
| DFM<br>(52)    | <b>GR18D5118</b><br>Advanced Computer<br>Aided Design                                  | <b>GR18D5123</b><br>Finite Element<br>Applications in<br>Manufacturing | <b>GR18D5120</b><br>Materials<br>Technology            | <b>GR18D5119</b><br>Advanced<br>Manufacturing<br>Processes  | <b>GR18D5012</b><br>Research Methodology and<br>IPR<br>(Common to All Programs) | <b>GR18D5207</b><br>English for Research<br>Paper Writing<br>(Common to All<br>Programs) |
| VLSI<br>(57)   | <b>GR18D5077</b><br>Digital CMOS IC<br>Design  | <b>GR18D5083</b><br>VLSI<br>Technology and<br>Design                   | <b>GR18D5078</b><br>Digital System<br>Design           | <b>GR18D5076</b><br>Digital System Design<br>using HDL  | <b>GR18D5012</b><br>Research Methodology and<br>IPR<br>(Common to All Programs) | <b>GR18D5207</b><br>English for Research<br>Paper Writing<br>(Common to All<br>Programs) |
| CSE<br>(58)    | <b>GR18D5001</b><br>Mathematical<br>Foundation for<br>Computer Science<br>Applications | <b>GR18D5002</b><br>Advanced Data<br>Structures                        | <b>GR18D5003</b><br>Advanced Data<br>Mining            | <b>GR18D5006</b><br>Computer System<br>Design<br><b>GR18D5007</b><br>Object Oriented<br>Modelling | <b>GR18D5012</b><br>Research Methodology and<br>IPR<br>(Common to All Programs) | <b>GR18D5207</b><br>English for Research<br>Paper Writing<br>(Common to All<br>Programs) |

**Note:** Any discrepancy may please be informed to COE immediately.

Date: 10-02-2025

  
Controller of Examinations